

WEST Search History

Hide Items

Restore

Clear

Cancel

DATE: Friday, October 22, 2004

09/9.15, 833

10-22-04

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR	
<input type="checkbox"/>	L35	l18 and L34	0
<input type="checkbox"/>	L34	l30 and L33	41
<input type="checkbox"/>	L33	queue\$1 or fifo\$1	96657
<input type="checkbox"/>	L32	l30 and L31	0
<input type="checkbox"/>	L31	message adj circuit\$1	743
<input type="checkbox"/>	L30	l28 and L29	66
<input type="checkbox"/>	L29	(microprocessors or processors) same (shared adj memor\$3)	7043
<input type="checkbox"/>	L28	l26 and L27	85
<input type="checkbox"/>	L27	l23 same l24	605
<input type="checkbox"/>	L26	l5 or l13 or l25	10659
<input type="checkbox"/>	L25	710/100-317.ccls.	8439
<input type="checkbox"/>	L24	shared adj memor\$3	16093
<input type="checkbox"/>	L23	system adj bus\$2	48360
<input type="checkbox"/>	L22	address and L21	2
<input type="checkbox"/>	L21	command\$1 and L20	2
<input type="checkbox"/>	L20	fifo and L19	5
<input type="checkbox"/>	L19	(shared adj memory) and L18	20
<input type="checkbox"/>	L18	(multiprocessor same message\$1).ti.	118
<input type="checkbox"/>	L17	L6 and L16	15
<input type="checkbox"/>	L16	pipeline and L15	18
<input type="checkbox"/>	L15	fifo and L14	52
<input type="checkbox"/>	L14	multiprocessor\$1 and L13	52
<input type="checkbox"/>	L13	L8 and L12	134
<input type="checkbox"/>	L12	(711/118 711/119 711/120 711/121 711/122 711/123 711/124 711/125 711/126 711/127 711/128 711/129 711/130 711/131 711/132 711/133 711/134 711/135 711/136 711/137 711/138 711/139 711/140 711/141 711/142 711/143 711/144 711/145 711/146 711/147 711/148 711/149 711/150 711/151 711/152 711/153 711/154 711/155 711/156 711/157 711/158 711/159 711/160 711/161 711/162 711/163 711/164 711/165 711/166 711/167 711/168 711/169).ccls.	13530
<input type="checkbox"/>	L11	L7 and L10	0
<input type="checkbox"/>	L10	circuit same L9	5
<input type="checkbox"/>	L9	pipeline same L8	60
<input type="checkbox"/>	L8	message\$1 same fifo	3009
<input type="checkbox"/>	L7	multiprocessor\$1 same (shared adj memor\$3)	2329

<input type="checkbox"/>	L6	multiprocessor\$1 and (shared adj memor\$3)	4155
<input type="checkbox"/>	L5	L4 or L3 or L2 or L1 <i>DB=USPT; PLUR=YES; OP=OR</i>	2256
<input type="checkbox"/>	L4	(712/246 712/247 712/248).cccls.	241
<input type="checkbox"/>	L3	(712/203).cccls.	103
<input type="checkbox"/>	L2	(712/32 712/33 712/34 712/35 712/36 712/37 712/38 712/39 712/40 712/41 712/42 712/43).cccls.	1534
<input type="checkbox"/>	L1	(719/312 719/313 719/314).cccls.	411

END OF SEARCH HISTORY